

10-997 U.S. PTO  
09/963590  
09/27/01

438	108	Subclass	ISSUE CLASSIFICATION
Class			

PATENT NUMBER

# U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

APPLICATION NO. 09/963590	CONT/PRIOR F	CLASS 257 438	SUBCLASS 678/08	ART UNIT 2844 1827	EXAMINER Grogan
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Mitsuru Komiyama  
Shinsuke Suzuki

Multi-chip package type semiconductor device

PTO-2040  
12/99

## ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
438	108	257	686	690	695	724	777
INTERNATIONAL CLASSIFICATION							
H01L	23/02						
H01L	23/48						
H01L	23/52						
H01L	23/39						
H01L	21/44						

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. 7	Figs. Drwg. 8	Print Fig. 1, 2A	Total Claims 18	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>  4/10/02	
	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b> Amount Due \$1,580.00	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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